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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Honda Yang

Assignee: Adaptec, Inc.

Title: PROGRAMMABLE LOGIC DEVICE PARTITIONING METHOD FOR
APPLICATION SPECIFIC INTEGRATED CIRCUIT
PROTOTYPING

Serial No.: 10/693,285 Filing Date: October 23, 2003

Examiner: Unassigned Group Art Unit: 2811

Docket No.: ADPT1070

Monterey, CA
May 5, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATUS INQUIRY

Dear Sir:

Applicant(s) have not received a first official action in the above application that was filed on October 23, 2003. What is the status of the above application and what is the expected date for issuance of a first official action?

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on May 5, 2005.

Respectfully submitted,

Forrest Gunnison
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Attorney for Applicant(s)

May 5, 2005

Date of Signature